

Appln No. 10/760,274  
Amdt. Dated February 6, 2006  
Response to Office Action of November 22, 2005

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**Amendments to the Specification:****The Paragraph beginning at Page 8, lines 28-29, is to be amended as follows:**

The ink jet printhead chip 52 (see Fig. 6) includes a silicon wafer substrate 80+8015. 0.35 Micron 1 P4M 12 volt CMOS microprocessing circuitry is positioned on the silicon wafer substrate 8015.